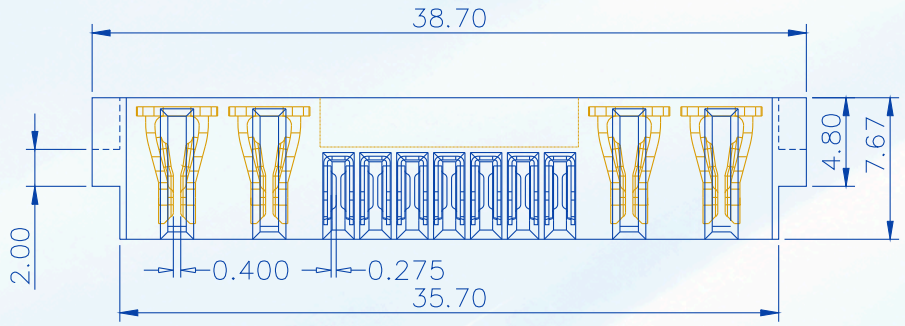
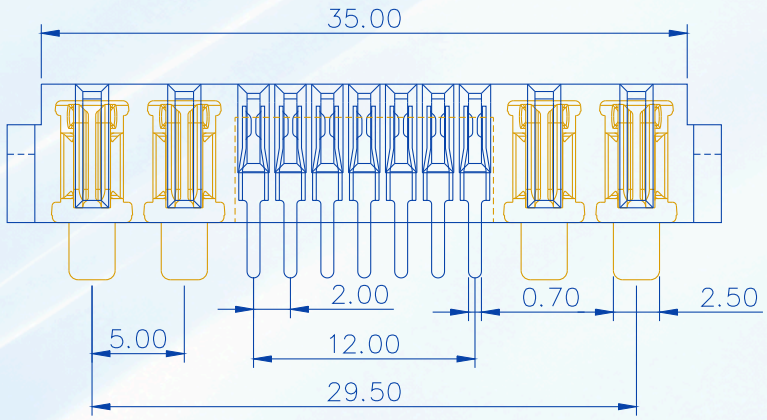
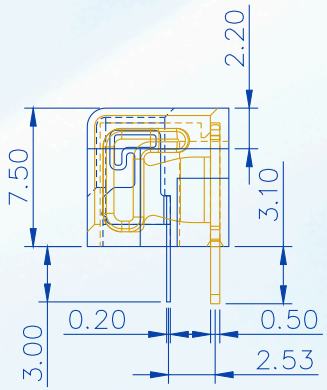
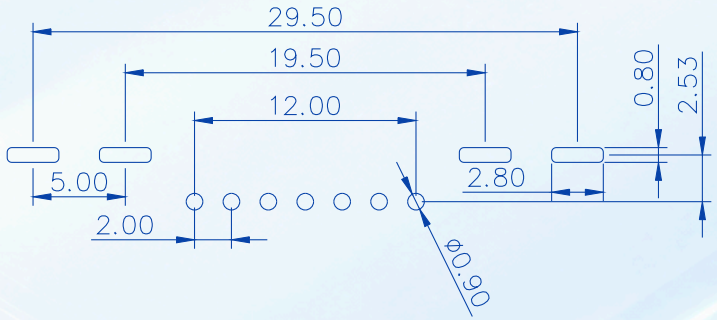








RECOMMEND PCB LAYOUT
Layout Tolerance = ±0.05mm

NOTES:
1. MATERIAL:
   INSULATOR: PA9T OR OTHER HIGH TEMP.
   PERFORMACE RESINS.
   CONTACT: PHOSPHOR BRONZE.C5191-H
2. FINISH: THIS AREA IS USED FOR SILVER AND GOLD PLATING
   TAIL TAIL IS SILVER-PLATED.
   WITH 50u"(min)NICKEL UNDER PLATING OVERALL.
3. PCB LAYOUT TOLERANCE: ±0.10
4. SPEC:Salt spray test 24hrs
5. Rating current:25A/Large PIN; Instant current:30A/Large PIN
6. ORDERING DATA:

LX-X XX XX X XX -X X

- Special NO:
- Tail type: T:180° S:90°
- Post position:
- Product altitude: 1:6.50mm 2:6.15mm 3:9.80mm 4:7.50mm
- Foolproof position: L2:Left second pin . L4:Left second pin R2:Right second pin . R4:Right second pin
- Amount of pins: 03:3pins . . 16:16pins
- Pitch: A:2.0mm B:2.5mm C:3.0mm D:5.0mm

UNLESS OTHERWISE SPECIFIED, TOLERANCE:
X. ±0.5 X.° ±1.5°
X.X ±0.25 X.X° ±1.0°
X.XX ±0.15 X.XX° ±0.5°
X.XXX ±0.05

深圳市康生电子科技有限公司
Shenzhen Kangsheng Electronic Technology Co., Ltd.

DESIGN		DATE		TITLE: 电池连接器
APPROVED		DATE		
MODEL: 5.0-2.0大电流母座11P		REV:	SCALE:	UNIT: mm
		SIZE: A4	SHEET: 1/1	